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LIST OF CURRENT CLAIMS

1. - 11. (Cancelled)

12. (Currently Amended) The time sharing multiplexing driving frame work for image signals of claim 11. A time-sharing multiplexing driving framework for image signals used for changing turned-on orders of control signal switches in a liquid crystal panel so as to make the turned-on orders of two adjacent phases in the panel the same and make no joint space generated in the panel, the framework comprising:

a plurality of wiring paths being a first wiring path, a second wiring path, a third wiring path and a fourth wiring path positioned on the same side of the panel and separately driven by a plurality of banks;

a plurality of control switches connected to the plurality of wiring paths, for controlling a plurality of pixel data voltages of a plurality of phases in the panel; and

a plurality of data line connectors connected to a plurality of data lines of the panel, the number of the connectors being the same as that of the wiring paths, wherein the plurality of phases connected to and controlled by the first switch of the first wiring path and the first switch of the second wiring path are adjacent to each other, and are turned on at the same time.

13. (Currently Amended) The time sharing multiplexing driving framework for image signals of claim 11. A time-sharing multiplexing driving framework for image signals used for changing turned-on orders of control signal switches in a liquid crystal panel so as to make the turned-on orders of two adjacent phases in the panel the same and make no joint space generated in the panel, the framework comprising:

a plurality of wiring paths being a first wiring path, a second wiring path, a third wiring path and a fourth wiring path positioned on the same side of the panel and separately driven by a plurality of banks;

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a plurality of control switches connected to the plurality of wiring paths, for

controlling a plurality of pixel data voltages of a plurality of phases in the panel; and

a plurality of data line connectors connected to a plurality of data lines of the

panel, the number of the connectors being the same as that of the wiring paths, wherein the

plurality of phases connected to and controlled by the first switch of the third wiring path

and the first switch of the fourth wiring path are adjacent to each other, and are turned on

at the same time.

14. (Cancelled)

15. (Cancelled)

16. (Currently Amended) The time-sharing multiplexing driving framework for

image signals of claim 15 A time-sharing multiplexing driving framework for image

signals used for changing turned-on orders of control signal switches in a liquid crystal

panel so as to make the turned-on orders of two adjacent phases in the panel the same and

make no joint space generated in the panel, the framework comprising:

a plurality of wiring paths being a first wiring path, a second wiring path, a third

wiring path and a fourth wiring path evenly positioned on two sides of the panel and

separately driven by a plurality of banks;

a plurality of control switches connected to the plurality of wiring paths, for

controlling a plurality of pixel data voltages of a plurality of phases in the panel; and

a plurality of data line connectors connected to a plurality of data lines of the

panel, the number of the connectors being the same as that of the wiring paths, wherein the

plurality of phases connected to and controlled by the first switch of the first wiring path

and the first switch of the fourth wiring path are adjacent to each other, and are turned on

at the same time.

17. (Cancelled)

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18. (Currently Amended) The time sharing multiplexing driving framework for image signals of claim 15 A time-sharing multiplexing driving framework for image signals used for changing turned-on orders of control signal switches in a liquid crystal panel so as to make the turned-on orders of two adjacent phases in the panel the same and make no joint space generated in the panel, the framework comprising:

a plurality of wiring paths being a first wiring path, a second wiring path, a third wiring path and a fourth wiring path evenly positioned on two sides of the panel and separately driven by a plurality of banks;

a plurality of control switches connected to the plurality of wiring paths, for controlling a plurality of pixel data voltages of a plurality of phases in the panel; and

a plurality of data line connectors connected to a plurality of data lines of the panel, the number of the connectors being the same as that of the wiring paths, wherein the plurality of phases connected to and controlled by the first switch of the second wiring path and the first switch of the fourth wiring path are adjacent to each other, and are turned on at the same time.

19. (Cancelled)

20. (Currently Amended) The time-sharing multiplexing driving framework for image signals of claim 19 A time-sharing multiplexing driving framework for image signals used for changing turned-on orders of control signal switches in a liquid crystal panel so as to make the turned-on orders of two adjacent phases in the panel the same and make no joint space generated in the panel, the framework comprising:

a plurality of wiring paths being a first wiring path, a second wiring path, a third wiring path, a fourth wiring path, a first wiring path, a sixth wiring path, a seventh wiring path and an eighth wiring path, evenly positioned on two sides of the panel and separately driven by a plurality of banks;

a plurality of control switches connected to the plurality of wiring paths, for controlling a plurality of pixel data voltages of a plurality of phases of the panel; and

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a plurality of data line connectors connected to a plurality of data lines of the panel, the number of the connectors being the same as that of the wiring paths, wherein the plurality of phases connected to and controlled by the first switch of the first wiring path and the first switch of the sixth wiring path are adjacent to each other, and are turned on at

the same time.

21. (Cancelled)

22. (Currently Amended) The time-sharing multiplexing driving framework for

image signals of claim 19 A time-sharing multiplexing driving framework for image

signals used for changing turned-on orders of control signal switches in a liquid crystal

panel so as to make the turned-on orders of two adjacent phases in the panel the same and

make no joint space generated in the panel, the framework comprising:

a plurality of wiring paths being a first wiring path, a second wiring path, a third

wiring path and a fourth wiring path evenly positioned on two sides of the panel and

separately driven by a plurality of banks;

a plurality of control switches connected to the plurality of wiring paths, for

controlling a plurality of pixel data voltages of a plurality of phases in the panel; and

a plurality of data line connectors connected to a plurality of data lines of the

panel, the number of the connectors being the same as that of the wiring paths, wherein the

plurality of phases connected to and controlled by the first switch of the third wiring path

and the first switch of the fourth wiring path are adjacent to each other, and are turned on

at the same time.

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